

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 7

Complete If Known

Application / C. nf. No.	10/075,178 / 7288
Filing Dat	February 14, 2002
First Named Invent r	Andy H. Gan
Art Unit	2825
Examiner Name	Binh C. Tat
Attorney Docket Number	X-1068 US

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. *	Document Number Number - Kind Code (If known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
VJ		US- 4,758,985	07-19-88	Carter	
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Examiner Initials *	Cite No. *	Foreign Patent Document Country Code * -Number * -Kind Code * (If known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T *
VJ		EP 0315275 A2	10-05-89	LSI Logic		
		WO 93 25968 A1	12-23-93	Furtek		
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		EP 1 235 351 A1	08-28-02	Matsushita Electric		

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Sheet	2	of	7	Attorney Docket Number	X-1068 US

OTHER -- NON PATENT LITERATURE DOCUMENTS			
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VS		SAYEE KIAEI et al., "VLSI DESIGN OF DYNAMICALLY RECONFIGURABLE ARRAY PROCESSOR-DRAP," IEEE, February 1989, pp. 2484-2488, V3.6, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
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		MICHAEL J. WIRTHLIN et al., "THE NANO PROCESSOR: A LOW RESOURCE RECONFIGURABLE PROCESSOR," IEEE, February 1994, pp. 23-30, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
		WILLIAM S. CARTER, "THE FUTURE OF PROGRAMMABLE LOGIC and ITS IMPACT ON DIGITAL SYSTEM DESIGN," April 1994, IEEE, pp. 10-16, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
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Examiner Signature	<i>WUTHE SUEK</i>	Date Considered	7/1/04
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Sheet	3	of	7
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		Number - Kind Code (if known)			
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OTHER - NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. *	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T *
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		XILINX, INC., "THE PROGRAMMABLE LOGIC DATA BOOK," 1994, Revised 1995, pp 2-109 to 2-117, Xilinx, Inc., 2100 Logic Drive, San Jose, CA. 95124.	
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VS		XILINX, INC., "THE PROGRAMMABLE LOGIC DATA BOOK," 2000, Ch 3, pp 3-7 TO 3-17; 3-76 TO 3-87, Xilinx, Inc., 2100 Logic Drive, San Jose, CA. 95124.	
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